

FIG. 1A is a block diagram of a network interface device 10. The device 10 includes a network interface 12, a backplane interface 14, a packet processing engine 16, and a plurality of MACs 18-1, 18-2, ..., 18-N. The network interface 12 is connected to a TO/FROM DATA NETWORK. The backplane interface 14 is connected to the packet processing engine 16. The packet processing engine 16 is connected to the MACs 18-1, 18-2, ..., 18-N. Each MAC 18-1, 18-2, ..., 18-N is connected to a plurality of N DOWNSTREAM CHANNELS 20. The N DOWNSTREAM CHANNELS 20 are connected to an UPCONVERTER 22, which is connected to an antenna 24. Each MAC 18-1, 18-2, ..., 18-N is also connected to a plurality of K UPSTREAM CHANNELS PER MAC 28. The K UPSTREAM CHANNELS PER MAC 28 are connected to a plurality of antennas 26-1, 26-2, ..., 26-K.

TO/FROM DATA NETWORK

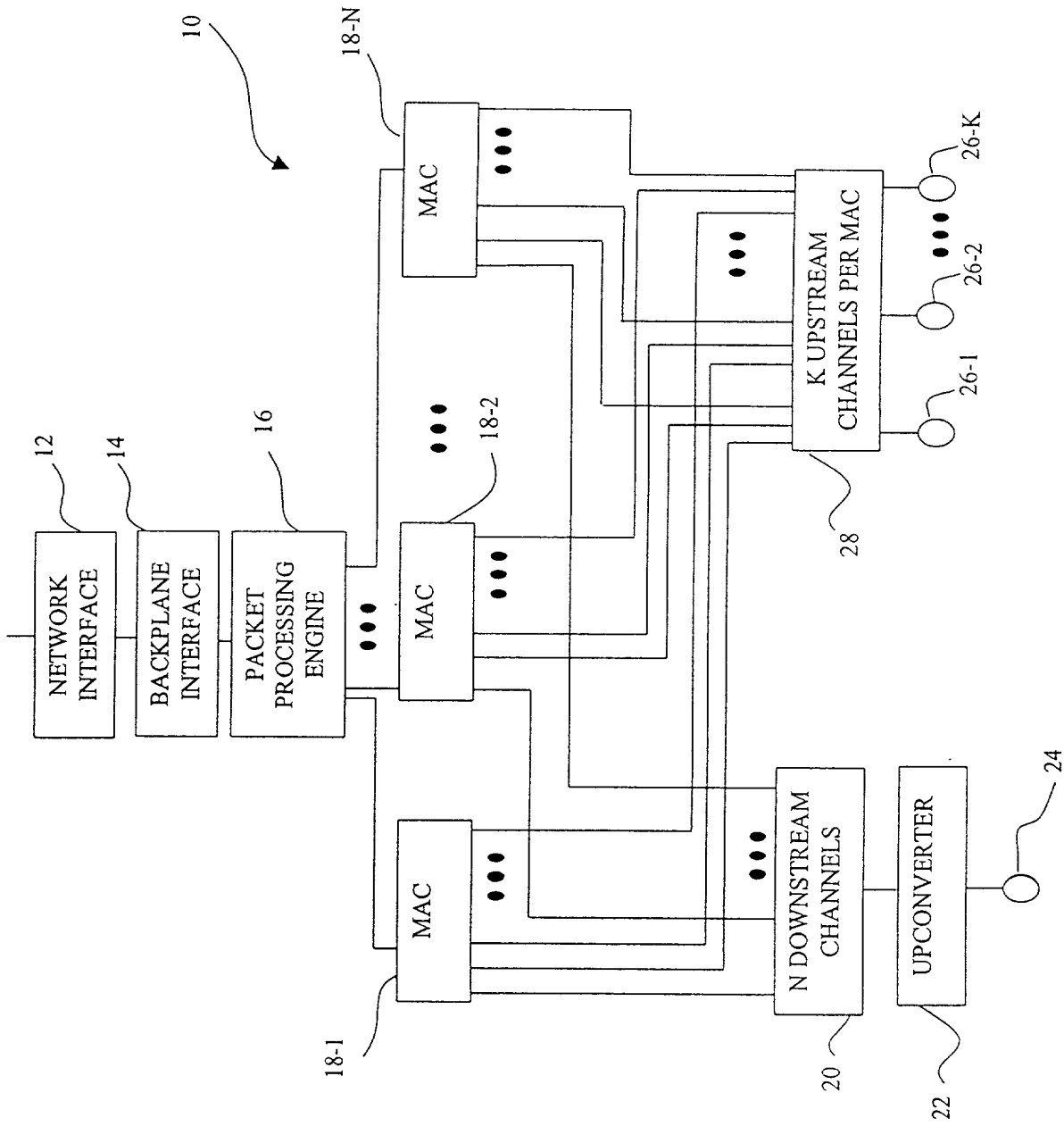


FIG. 1A 1/6

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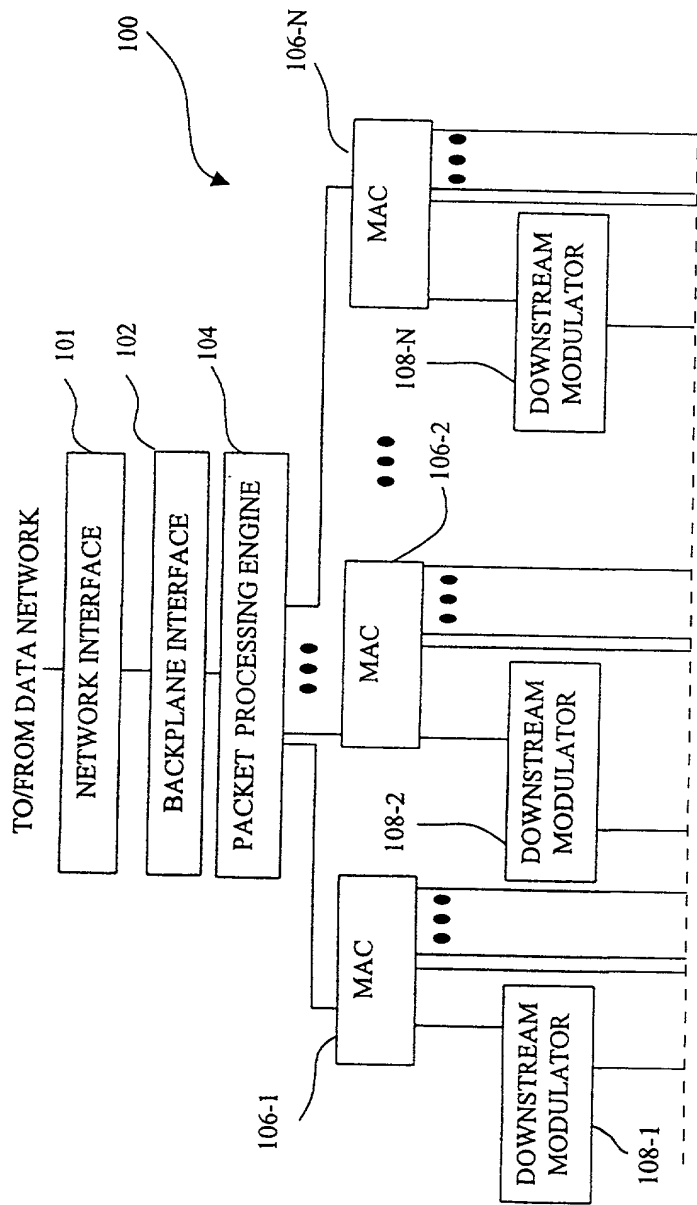
FIG. 1B₁

FIG. 1B₂

FIG. 1B

FIG. 1B₁ is a perspective view of a first embodiment of a device in a first state. FIG. 1B₂ is a perspective view of the device in a second state. FIG. 1B is a perspective view of the device in a third state.

FIG. 1B₁



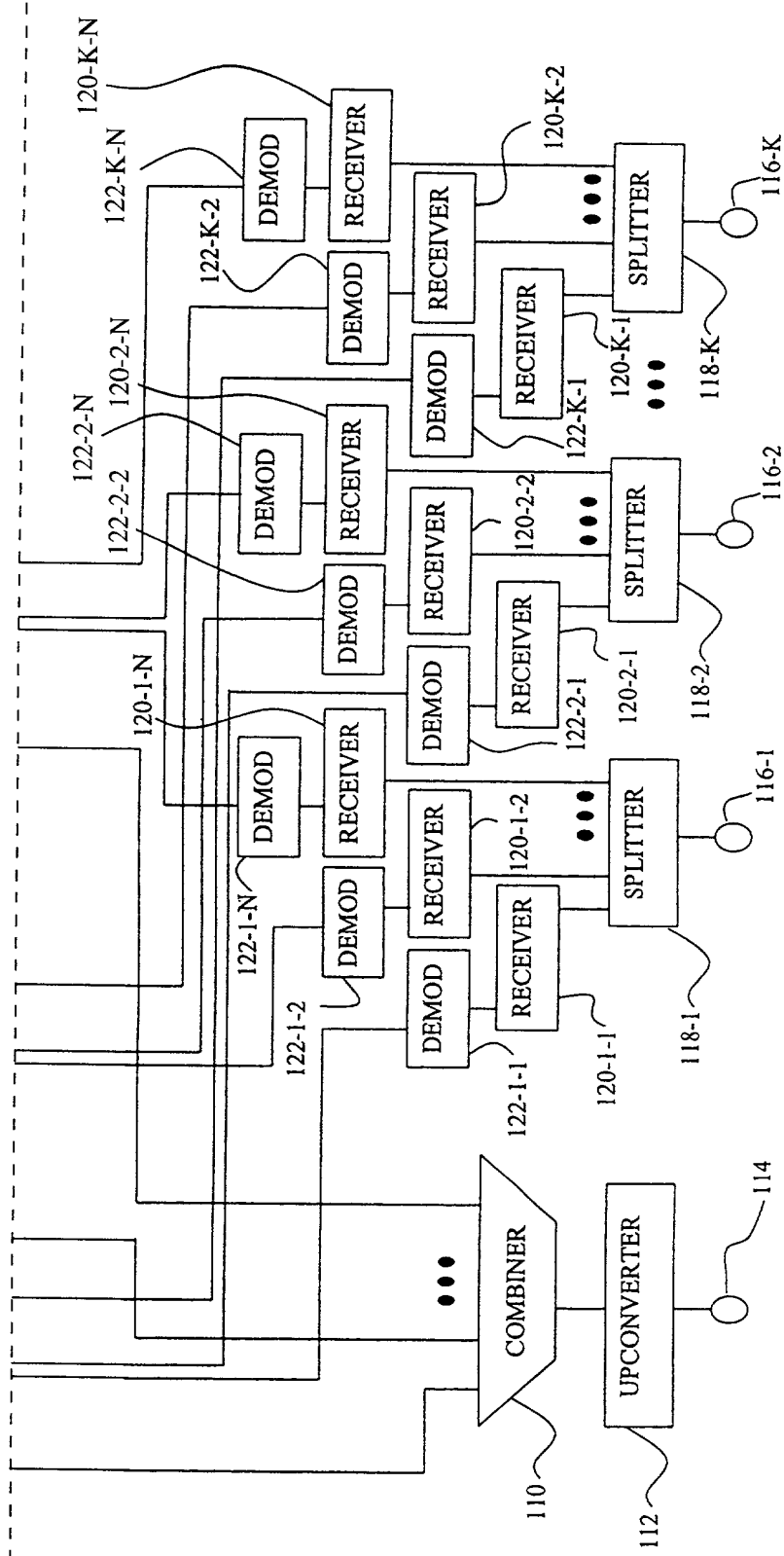


FIG. 1B₂

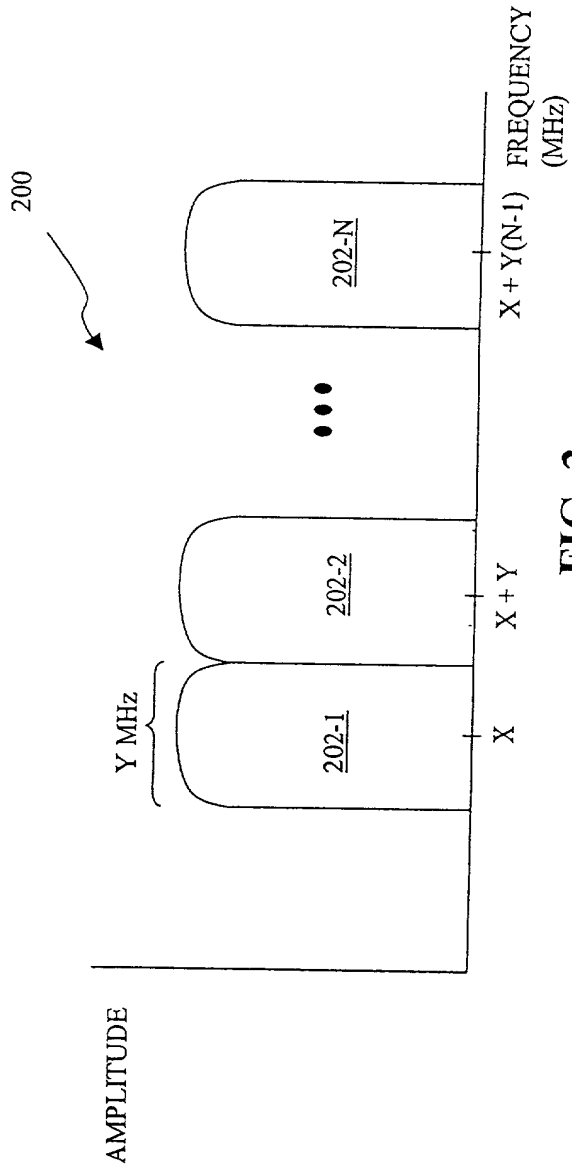


FIG. 2

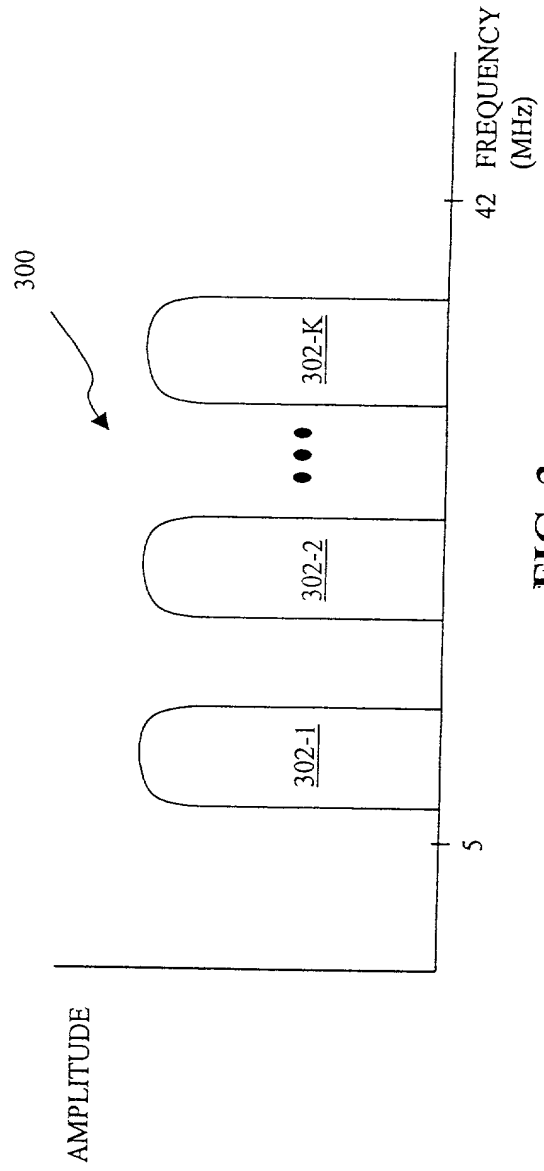


FIG. 3

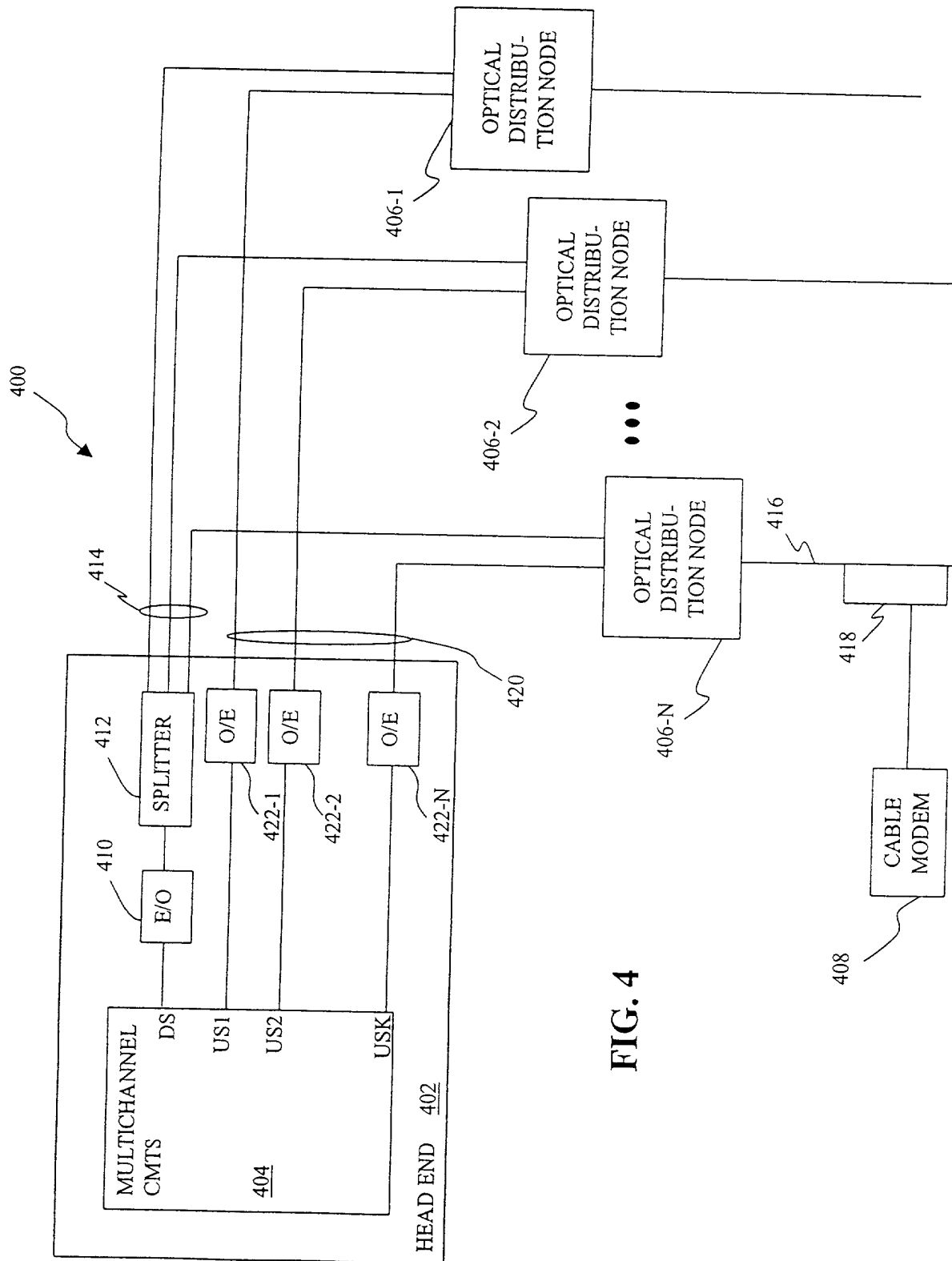


FIG. 4